

Recent IBM patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Canada

2,026,998 D. K. Smith and T. E. Glover Dynamic Trace Elements

France

387,464 A. Tracol, D. Orsatti, G. Barucchi, and J. Calvignac Switching System for Simultaneously Transferring Data Between Data Processing Units
407,673 D. G. Maiwald, E. Eleftheriou, M. Quintin, and P. R. Chevillat Process of Synchronizing a Receiving Modem after a Training on Data

Germany

4,307,208.9 F. Schlegel and U. Abendschein Box (Computer Box)

United Kingdom

321,080 A. R. Halbert Solid Modelling System
429,728 A. C. Gay, K. R. Barraclough, N. D. Butler, and R. B. Harrison Logic Circuit

United States

5,262,643 J. M. Hammond, M. A. Klos, Y. Martin, K. G. Roessler, and R. M. Stowell Automatic Tip Approach Method and Apparatus for Scanning Probe Microscope
5,262,690 D. B. Cochran, M. C. Lee, and K. M. Owczarski Variable Delay Clock Circuit
5,262,719 S. Magdo Test Structure for Multi-layer, Thin-Film Modules
5,262,737 M. M. Siverling Data Processing System and Housing Having Reduced Electromagnetic Emissions
5,262,847 A. A. Rodriguez, M. A. Pietras, and A. J. Saenz Method of Converting Luminance-Color Difference Video Signal to a Three Color Component Video Signal
5,262,860 G. P. Fitzpatrick, W. J. Johnson, R. S. Keller, and M. L. Williams Method and System Communication Establishment Utilizing Captured and Processed Visually Perceptible Data within a Broadcast Video Signal
5,262,904 Y. S. Tang and C. H. Tsang Method and Apparatus for Measuring and Compensating Nonlinear Bitshift
5,262,914 M.-M. Chen and C. H. Tsang Magnetoresistive Head with Enhanced Exchange Bias Field
5,262,964 G. W. Bonsall, A. G. Cripps, R. A. Pascoe, and C. L. Peek Method and Apparatus for Variable Playback Speed of Multimedia Data Interchange within a Data Processing System
5,263,028 P. Borgnis, J. Cresp, and R. Moreau Frame Transfer Device for a Fixed Format Frame Transmission Network

©Copyright 1994 by International Business Machines Corporation.

641

5,263,054	G. T. Davis, B. D. Mandalia, J. C. Sinibaldi, and W. M. Zevin	Method and System for Interpolating Baud Rate Timing Recovery for Asynchronous Start Stop Protocol
5,263,117	A. J. Nadas and D. Nahamoo	Method and Apparatus for Finding the Best Splits in a Decision Tree for a Language Model for a Speech Recognizer
5,263,145	J. T. Brady, R. E. Cintron, S. Goldstein, J. H. W. Ju, and J. M. Menon	Method and Means for Accessing DASD Arrays with Tuned Data Transfer Rate and Concurrency
5,263,149	T. Winlow	Integrated Circuit Logic Functions Simulator for Selectively Connected Series of Preprogrammed PLA Devices Using Generated Sequence of Address Signals Being Provided Between Simulated Clock Cycles
5,263,154	L. E. Eastridge, R. F. Kern, and J. M. Ratliff	Method and System for Incremental Time Zero Backup Copying of Data
5,263,157	F. L. Janis	Method and System for Providing User Access Control within a Distributed Data Processing System by the Exchange of Access Control Profiles
5,263,158	F. L. Janis	Method and System for Variable Authority Level User Access Control in a Distributed Data Processing System Having Multiple Resource Manager
5,263,159	K. Mitsui	Information Retrieval Based on Rank-Ordered Cumulative Query Scores Calculated from Weights of all Keywords in an Inverted Index File for Minimizing Access to a Main Database
5,263,165	F. L. Janis	System for Providing User Access Control within a Distributed Data Processing System Having Multiple Resource Managers
5,263,167	H. K. Conner, Jr., D. G. Petersen, J. S. Wang, and R. B. Wood	User Interface for a Relational Database Using a Task Object for Defining Search Queries in Response to a Profile Object Which Describes User Proficiency
5,263,172	H. T. Olnowich	Multiple Speed Synchronous Bus Having Single Clock Path for Providing First or Second Clock Speed Based Upon Speed Indication Signals
5,263,245	M. S. Patel and J. J. Zopff	Method of Making an Electronic Package with Enhanced Heat Sinking
5,263,537	M. D. Plucinski and W. C. Miller	Oscillating Cooling System
5,263,620	B. Hernandez, R. R. Horton, I. C. Noyan, M. J. Palmer, and M. B. Ritter	Wirebond Removal Apparatus Using Alternating Fluid Stream
5,263,776	D. W. Abraham, W. M. Holber, J. S. Logan, and H. K. Wickramasinghe	Multi-wavelength Optical Thermometry
5,264,325	R. D. Allen, R. A. Day, D. H. Glatzel, W. D. Hinsberg, J. R. Mertz, D. J. Russell, and G. M. Wallraff	Composition for Photo Imaging
5,264,328	R. A. DellaGuardia, J. L. Mauer IV, and D. E. Seeger	Resist Development Endpoint Detection for X-ray Lithography
5,264,387	K. D. Beyer, M. A. Jaso, S. S. Iyer, S. R. Stiffler, and J. D. Warnock	Method of Forming Uniformly Thin, Isolated Silicon Mesas on an Insulating Substrate
5,264,395	A. Bindal, C. Galli, and N. Rovedo	Thin SOI Layer for Fully Depleted Field Effect Transistors
5,264,399	J. A. Casey and S. M. DeCarr	Ceramic Composite Body
5,264,664	M. F. McAllister and J. A. McDonald	Programmable Chip to Circuit Board Connection
5,264,716	D. M. Kenney	Diffused Buried Plate Trench DRAM Cell Array
5,264,780	R. E. Bruer and S. F. Newton	On Time Control and Gain Circuit
5,264,782	S. F. Newton	Dropout Recovery Circuit
5,264,837	M. J. Buehler	Video Insertion Processing System
5,264,842	P. A. Franaszek	Generalized Usage of Switch Connections with Wait Chain
5,264,958	D. W. J. Johnson	Universal Communications Interface Adaptable for a Plurality of Interface Standards
5,264,975	C. H. Bajorek, T. W. Glaser, R. Greenberg, J. L. Neubauer, and J. R. Reidenbach	Magnetic Disk Storage Device with the Base Provided by an Electronic Module Substrate
5,264,981	R. T. Campbell and J.-S. J. Lo	Multilayered Ferromagnetic Film and Magnetic Head Employing the Same
5,265,056	E. Butler, W. F. Ellis, T. M. Redman, and E. P. Thoma	Signal Margin Testing System for Dynamic RAM
5,265,082	J. J. Gniewek and D. C. Graves	Rewritable Media Protectable as Written-Once-Only Media and System and Method for Use Therewith
5,265,085	G. A. Jaquette and M. Tayefeh	Simultaneous MO and ROM Optical Disk Signal Detection
5,265,098	R. L. Mattson and S. Ng	Method and Means for Managing DASD Array Accesses When Operating in Degraded Mode
5,265,157	C. H. Jolissaint and G. W. Kerlin	Answer Supervision and Service Selection
5,265,163	V. G. Golding and G. H. Speirs	Computer System Security Device
5,265,164	S. M. Matyas, D. B. Johnson, A. V. Le, R. Prymak, W. C. Martin, W. S. Rohland, and J. D. Wilkins	Cryptographic Facility Environment Backup/Restore and Replication in a Public Key Cryptosystem

5,265,193	C. R. Grady and G. Greenwald	Efficiently Organizing Objects in a RETE Pattern Matching Network
5,265,198	J. Gonzalez-Lopez and T. P. Lanzoni	Method and Processor for Drawing 'Polygon with Edge'-Type Primitives in a Computer Graphics Display System
5,265,200	A. D. Edgar	System and Method for Automatic Image Saturation, Gamma, and Exposure Correction in a Digitizing Video Capture System
5,265,202	L. A. Krueger, E. S. Rozmiarek, and J. G. Tench, Jr.	Method and System for Accessing Visually Obscured Data in a Data Processing System
5,265,206	F. W. Shackelford, C. A. Ross, W. L. Rich, R. L. Abraham, J. R. Tiller, Jr., R. E. Moore, and R. S. Briggs, Jr.	System and Method for Implementing a Messenger and Object Manager in an Object Oriented Programming Environment
5,265,211	N. Amini, B. F. Boury, R. L. Horne, and T. J. Lohman	Arbitration Control Logic for Computer System Having Dual Bus Architecture
5,265,215	M. Fukuda, N. Ooba, and T. Nakada	Multiprocessor System and Interrupt Arbiter Thereof
5,265,232	P. M. Gannon, M. Ignatowski, M. A. Krygowski, L. Liu, D. W. Price, W. K. Rodiger, G. Salyer, Y.-M. Ting, and M. P. Witt	Coherence Control by Data Invalidation in Selected Processor Caches without Broadcasting to Processor Caches Not Having the Data
5,265,238	F. J. Canova, Jr., N. A. Katz, S. Astarabadi, and R. L. Horton	Automatic Device Configuration for Dockable Portable Computers
5,265,240	R. E. Galbraith, S. G. Glassen, A. Marron, K. J. Oakes, D. E. Stucki, and L. W. Wyman	Channel Measurement Method and Means
5,265,241	L. Arnold, G. A. Fisher, Sr., and D. B. Flaxer	Method and Apparatus for Verifying the Configuration of a Link-Connected Network
5,265,244	S. P. Ghosh and R. A. C. Lorie	Method and System for Facilitating Processing of Statistical Inquires on Stored Data Accessible Through a Data Access Structure
5,265,245	M. L. Nordstrom, G. R. Ricard, J. J. Vriezen, D. R. Welsh, and L. W. Youngren	High Concurrency in Use Manager
5,265,246	S.-G. Li and B. A. Tate	Graphic Definition of Range in the Selection of Data from a Database Field
5,265,251	H. C. Agarawal and R. L. Verburg	Mechanism for Allowing a Single Operation to Shift the Focus Between User Applications Having Direct Hardware Level Access to Multiple Displays in a Virtual Terminal Environment
5,265,252	F. L. Rawson III, G. G. Sotomayor, Jr., and E. M. Tuggle, Jr.	Device Driver System Having Generic Operating System Interface
5,265,255	F. M. Bonevento, E. N. Mandese, and R. N. Mendelson	Personal Computer System with Interrupt Controller
5,265,260	J. E. Hendricks	High Performance Sort Hardware for a Database Accelerator in a Data Processing System
5,265,670	A. R. Zingher	Convection Transfer System
5,266,444	B. J. Carpenter, Jr., J. LaTorre, M. G. McMaster, and L. L. Simpson	Method and Composition for Obtaining Image Reversal in Epoxy Formulations Based Upon Photoinhibition
5,266,446	K. Chang, G. Czornyj, M. S. Farooq, A. H. Kumar, M. S. Pitler, and H. O. Steimel	Method of Making a Multilayer Thin Film Structure
5,266,504	J. L. Blouse, J. O. Chu, B. Cunningham, J. P. Gambino, L. L. Hsu, D. E. Kotecki, S. Subbanna, and Z.-J. Tien	Low Temperature Emitter Process for High Performance Bipolar Devices
5,266,505	D. C. Ahlgren, S.-F. S. Chu, M. J. Saccamango, D. A. Sunderland, and T.-C. Chen	Image Reversal Process for Self-aligned Implants in Planar Epitaxial-Base Bipolar Transistors
5,266,520	T. M. Cipolla, P. W. Coteus, R. H. Katyl, R. J. Kelleher, and P. A. Moskowitz	Electronic Packaging with Varying Height Connectors
5,266,522	G. DiGiacomo, A. S. Cammarano, and N. DiPaolo	Structure and Method for Corrosion and Stress-Resistant Interconnecting Metallurgy
5,266,558	F. Lichtenberg, J. Mannhart, and D. Schlom	Superconducting Circuit Elements with Metallic Substrate and Method for Manufacturing the Same
5,266,769	A. P. Deshpande, R. T. Kerth, and H. M. Nier	Process for Independent Control of Crown and Camber for Magnetic Head Slider
5,266,805	A. D. Edgar	System and Method for Image Recovery
5,266,813	J. H. Comfort, D. L. Harame, and S. R. Stiffler	Isolation Technique for Silicon Germanium Devices
5,266,850	H. A. Au, A. M. Patel, R. A. Rutledge, B. S. So, and A. S. Su	Clock Delay Trim Adjustment with Stopping Feature for Eliminating Differential Delay Between Clock Signal and Analog Signal
5,266,896	D. Rugar, J. A. Sidles, and C. S. Yannoni	Mechanical Detection and Imaging of Magnetic Resonance by Magnetic Moment Modulation
5,266,897	O. Watanuki, F. Sai, and K. Sueoka	Magnetic Field Observation with Tunneling Microscopy
5,266,901	K.-C. Woo	Apparatus and Method for Resistive Detection and Waveform Analysis of Interconnection Networks

5,266,933	D. C. Frank, S. Matsubara, H. Satoh, and S. P. Thompson	Method and Apparatus for Displaying a Screen Separator Line
5,267,047	J. T. Argenta, D. C. Bailey, H. F. DeBruyn, and H. M. Morris	Apparatus and Method of Operation for a Facsimile Subsystem in an Image Archiving System
5,267,106	B. M. Brue, P. M. Herman, J. L. Neubauer, and S. H. Voss	System for Clamping a Plurality of Disks to a Hub
5,267,110	H. H. Ottesen and M. Sri-Jayantha	Rotary Actuator for a Direct Access Storage Device
5,267,127	R. F. Pollitt	Personal Computer with Folding and Sliding Keyboard
5,267,156	H. Nomiya	Method for Constructing a Knowledge Base, Knowledge Base System, Machine Translation Method and System Therefor
5,267,216	B. Gabillard, P. Girard, and M. Grandguillot	BICMOS Local Address Transition Detection Circuit
5,267,227	K. Nanke	Disk Storage Apparatus with an Actuator Locking Device
5,267,240	N. G. Bartow, P. J. Brown, R. S. Capowski, L. T. Fasano, T. A. Gregg, G. Salyer, P. J. Sugrue, D. W. Westcott, and V. P. Zeyak, Jr.	Frame-Group Transmission and Reception for Parallel/Serial Buses
5,267,242	R. W. Lavallee, D. G. O'Brien, M. Rubino, W. W. Shen, and G. C. Wellwood	Method and Apparatus for Substituting Spare Memory Chip for Malfunctioning Memory Chip with Scrubbing
5,267,246	K. C. Huang, J. G. Santoni, and G. S. Still	Apparatus and Method for Simultaneously Presenting Error Interrupt and Error Data to a Support Processor
5,267,345	P. F. Brown, S. A. Della Pietra, V. J. Della Pietra, R. L. Mercer, P. S. Resnik, and S. S. Chen	Speech Recognition Apparatus Which Predicts Word Classes from Context and Words from Word Classes
5,267,418	J. E. Currie, R. N. Schulz, and A. D. Ticknor	Confined Water Fixture for Holding Wafers Undergoing Chemical-Mechanical Polishing
5,267,471	D. W. Abraham and M. P. O'Boyle	Double Cantilever Sensor for Atomic Force Microscope
5,267,737	L. A. Cossette and C. A. Delagardelle	Magnetic Fluid Seal Containment Shield
5,268,068	D. R. Cowell, M. W. Jones, C.-C. Kin, J. J. Nahlik, and J. A. Trumpetto	High Aspect Ratio Molybdenum Composite Mask Method
5,268,069	J. D. Chapple-Sokol, R. A. Conti, D. E. Kotecki, A. H. Simon, and M. Tejwani	Safe Method for Etching Silicon Dioxide
5,268,072	B. N. Agarwala, M. Datta, R. E. Gegenwarth, C. V. Jahnes, P. M. Miller, H. A. Nye III, J. F. Roeder, and M. A. Russak	Etching Processes for Avoiding Edge Stress in Semiconductor Chip Solder Bumps
5,268,207	B. P. Kruger, T. S. Petersen, and S. F. Starcke	Texturing the Surface of a Recording Disk Using Particle Impact
5,268,260	N. R. Bantu, A. C. Bhatt, A. C. Bhatt, J. A. Kotylo, G. W. Jones, R. J. Owen, K. Papathomas, and A. K. Vardya	Photoresist Develop and Strip Solvent Compositions and Method for Their Use
5,268,311	W. Euen, D. Hagmann, and H.-J. Wildau	Method for Forming a Thin Dielectric Layer on a Substrate
5,268,324	J. M. Aitken, V. P. Kesan, S. Subbanna, M. J. Tejwani, and S. S. Iyer	Modified Silicon CMOS Process Having Selectively Deposited Si/SiGe FETS
5,268,330	J. H. Givens, J. S. Nakos, P. A. Burke, C. M. Hill, and C. H. Lam	Process for Improving Sheet Resistance of an Integrated Circuit Device Gate
5,268,592	P. D. Bellamy and T. T. Takayesu	Sequential Connector
5,268,735	R. Hayashi	Apparatus for Observing Features and Defects of a Sample
5,268,804	C. N. Wallis	Actuator Arm Controller for a Storage Device
5,268,806	W. M. Goubau, J. K. Howard, H.-C. W. Huang, C. Hwang, R. O. Schwenker, and J. C. Uy	Magnetoresistive Transducer Having Tantalum Lead Conductors
5,268,808	E. R. Childers	Disk Cartridges Made from Unitary Blanks
5,268,813	S. G. Chapman	Flexible Printed Circuit Package and Flexible Printed Circuit for Incorporating in Such a Package
5,268,814	C. Yakubowski	Module Packaging
5,268,815	T. M. Cipolla, P. W. Coteus, B. C. Derdall, C. M. Knoedler, A. P. Lanzetta, J. J. Liutkus, L. C. Matthew, L. S. Mok, and I. A. Sterian	High Density, High Performance Memory Circuit Package
5,268,846	G. W. Bonsall, A. G. Cripps, R. A. Pascoe, and C. L. Peek	Method and Apparatus for Nonsequential Multimedia Data Interchange in a Data Processing System
5,268,848	J. D. Coker, R. L. Galbraith, and G. W. Walker	Equalizer Adjustment for Partial-Response Maximum-Likelihood Disk Drive Systems
5,268,871	S. H. Dhong, H. J. Shin, and W. Hwang	Power Supply Tracking Regulator for a Memory Array

5,268,893	D. E. Call and L. D. Tipton	Write Power Calibration Utilizing Least Squares Fit of Read-Back Signals for Moving Media Memory
5,268,922	J.-C. Fouere and C. Metreaud	Laser Diode Assembly
5,268,938	E. Feig and F. C. Mintzer	Redundancy Scheme for Fourier Transform Coding on Peak Limited Channels
5,268,951	A. L. Flamholz, R. P. Rippstein, J. P. Silverman, and M. A. Thompson	X-ray Beam Scanning Method for Producing Low Distortion or Constant Distortion in X-ray Proximity Printing
5,269,004	L. D. Comerford and J. J. Laibinis	System for Integrating Pointing Functions into Computer Keyboard with Lateral Movement of Keyswitch Mounting Plate Causing Strain and Control Signal
5,269,009	R. D. Herzl, K. A. Lauricella, L. L. Quinn, D. A. Schroter, A. R. Steel, and J. L. Temple III	Processor System with Improved Memory Transfer Means
5,269,017	C. O. Hayden, R. J. Hurban, D. W. McCauley, J. S. Murdock, Jr., and S. B. Stillman	Type 1, 2 and 3 Retry and Checkpointing
5,269,213	A. F. Coneski, D. J. Sykora, and M. Waian	Punch Apparatus
5,269,372	R. C. Chu, M. J. Ellsworth, Jr., R. E. Simons, and D. T. Vader	Intersecting Flow Network for a Cold Plate Cooling System
5,270,151	P. A. Agostino, A. P. Giri, H. Hiraoka, C. G. Willson, and D. J. Dawson	Spin on Oxygen Reactive Ion Etch Barrier
5,270,234	D. L. Huang, L. L. Hsu, and W.-Y. Wang	Deep Submicron Transistor Fabrication Method
5,270,261	C. L. Bertin, P. A. Farrar, Sr., H. L. Kalter, G. A. Kelley, Jr., W. B. van der Hoeven, and F. R. White	Three Dimensional Multichip Package Methods of Fabrication
5,270,712	B. R. Iyer, G. G. Langdon, Jr., and A. Zandi	Sort Order Preserving Method for Data Storage Compression
5,270,874	P. J. Seger	Dropout Simulation in a Magnetic Tape Subsystem
5,270,880	H. H. Ottesen, A. Sharma, and M. Sri-Jayantha	Predictive Read-Write Inhibit Decision Using Fuzzy Logic
5,270,882	S. A. Jove, K. B. Klaassen, and J. C. L. van Peppen	Low-Voltage, Low-Power Amplifier for Magnetoresistive Sensor
5,270,902	R. J. Bellar, S. J. Kim, and A. L. Murphy	Heat Transfer Device for Use with a Heat Sink in Removing Thermal Energy from an Integrated Circuit Chip
5,271,000	A. J. Engbersen, M. Heddes, A. Herkersdorf, R. Luijten, and E. Rothauser	Method and Apparatus for Testing and Evaluation of Distributed Networks
5,271,012	M. M. Blaum, H. T. Hao, R. L. Mattson, and J. M. Menon	Method and Means for Encoding and Rebuilding Data Contents of Up To Two Unavailable DASDs in an Array of DASDs
5,271,014	J. Bruck, R. E. Cypher, and C.-T. Ho	Method and Apparatus for a Fault-Tolerant Mesh with Spare Nodes
5,271,075	F. Gfeller, P. L. Heinzmann, O. Martin, and J. R. Mueller	Fiber Optic Switch with Remote Optical Powering
5,271,091	F. D. Highland, C. T. Iwaskiw, and J. D. Tani	Asynchronous Architecture for Knowledge-Based Systems
5,271,094	V. A. Albaugh and R. J. Urquhart	Z-Buffer Quantization of Three-Dimensional Lines
5,271,097	B. A. Barker, T. R. Edel, and J. A. Stark	Method and System for Controlling the Presentation of Nested Overlays Utilizing Image Area Mixing Attributes
5,271,516	L. G. Cook, R. F. Florence, Jr., G. M. Gallagher, G. E. Johnson, and R. W. Sargent	Isolation Structure for Contamination Sensitive Items
5,271,802	H. C. Chang, M.-M. Chen, C. T. Horng, and R. O. Schwenker	Method for Making a Thin Film Magnetic Head Having a Protective Coating
5,272,024	B. J. Lin	Mask-Structure and Process to Repair Missing or Unwanted Phase-Shifting Elements
5,272,042	R. D. Allen, W. R. Brunsvold, B. J. Carpenter, W. D. Hinsberg, J. LaTorre, M. G. McMaster, M. W. Montgomery, W. M. Moreau, L. L. Simpson, R. J. Twig, and G. M. Wallraff	Positive Photoresist System for Near-UV to Visible Imaging
5,272,373	H. Baratte, J. P. de Souza, and D. K. Sadana	Internal Gettering of Oxygen in III-V Compound Semiconductors
5,272,397	I. P. Chen, J. W. Davis, R. M. Swanson, N. G. Thoma, and D. M. Wu	Basic DCVS Circuits with Dual Function Load Circuits
5,272,470	J. M. Zetts	Apparatus and Method for Reducing System Overhead While Inking Strokes in a Finger or Stylus-Based Input Device of a Data Processing System
5,272,471	S. Asada, M. Sohda, and H. Yasuda	Display System
5,272,550	L. D. Dickson and F. S. Luecke	Hologram System

5,272,584	P. Austruy, J.-P. Conjeaud, P. Massi, and C. Jacquart	Hot-Plugging Circuit for the Interconnection of Cards to Boards
5,272,690	R. A. Bargerhuff, L. D. Dickson, and M. C. Krantz	Hologram Element System
5,272,692	I. E. Henderson and M. Tayefeh	Data Storage Member Having Areas in Which Intensity-Modulated Signals Are Overlaid with Magneto-optically Recorded Signals
5,272,729	R. Bechade, F. D. Ferraiolo, B. Kaufmann, I. I. Novof, S. F. Oakland, K. Shaw, and L. Skarshinski	Clock Signal Latency Elimination Network
5,273,829	E. Bassous, B. S. Meyerson, and K. J. Uram	Epitaxial Silicon Membranes
5,273,856	C. F. Lyons, S. E. Perreault, G. T. Spinillo, and R. L. Wood	Positive Working Photoresist Composition Containing Mid or Near UV Radiation Sensitive Quinone Diazide and Sulfonic Acid Ester of Imide or Oxime Which Does Not Absorb Mid or Near UV Radiation
5,273,913	S. Divakaruni, B. El-Kareh, and E. D. Johnson	High Performance Lateral PNP Transistor with Buried Base Contact
5,274,267	T. W. Moksvold	Bipolar Transistor with Low Extrinsic Base Resistance and Low Noise
5,274,285	D. W. Boerstler, E. B. Eichelberger, G. T. Hendrickson, and C. B. Winn	Enhanced Differential Current Switch Compensating Upshift Circuit
5,274,420	J.-C. A. Chastang and A. E. Rosenbluth	Beamsplitter Type Lens Elements with Pupil-Plane Stops for Lithographic Systems
5,274,569	M. K. Prasad	Dual Sense Non-differencing Digital Peak Detector
5,274,625	J. H. Derby, R. Guérin, and L. Gün	Traffic Measurements in Packet Communications Networks
5,274,646	T. M. Brey, M. A. Krygowski, B. L. McGilvray, T. H. Nguyen, W. W. Shen, and A. J. Sutton	Excessive Error Correction Control
5,274,648	R. G. Eikill, S. J. Finnes, C. P. Geer, and Q. G. Schmieser	Memory Card Resident Diagnostic Testing
5,274,758	B. J. Beitel, M. S. Bishop, N. A. Burns, J. J. Deacon, R. D. Gordon, C. L. Haug, K. B. Smith, L. S. Walling, M. D. Wilkes, and P. C. Yanker	Computer-Based, Audio/Visual Creation and Presentation System and Method
5,274,760	B.-O. Schneider	Extendable Multiple Image-Buffer for Graphics Systems
5,274,772	E. C. Dunn, S. M. Fry, S. A. Jackson, N. H. MacLean, Jr., R. P. Reynolds, and R. A. Ripberger	Data Processing Systems Having Means for Selectively Combining Records into Signal Blocks for Recording in a Record Medium
5,274,782	S. Chalasani and A. M. Varma	Method and Apparatus for Dynamic Detection and Routing of Non-uniform Traffic in Parallel Buffered Multistage Interconnection Networks
5,274,801	O. M. Gordon, J. R. Hubbell, and N. J. Woodland	Artificial Intelligence Delivery System
5,274,820	M. J. L. Gillet	Method and System for Eliminating Operation Codes from Intermediate Prolog Instructions
5,274,821	G. J. A. Rouquie	Communication Between Prolog and an External Process
5,274,823	L. B. Brenner, B. P. Lubart, J. S. Lucash, J. C. Rathjen, Jr., R. Sasala, and T. Van Weaver	Interrupt Handling Serialization for Process Level Programming
5,274,841	K. S. Natarajan, S. R. Chaudhuri, and P. D. Hortensius	Methods for Polling Mobile Users in a Multiple Cell Wireless Network
5,274,913	K. R. Grebe, J. M. McCreary, D. Suryanarayana, and H.-M. Tong	Method of Fabricating a Reworkable Module
5,275,330	P. D. Isaacs, G. A. Knotts, M. F. Swain, and B. A. Towne	Solder Ball Connect Pad-on-Via Assembly Process
5,275,495	J. T. Wilson III	Ribbon Re-inking Mechanism
5,275,695	L. S. Chang, J. M. Eldridge, C. Hwang, J. H. Lee, and T.-G. Wang	Process for Generating Beveled Edges
5,275,705	D. S. Bethune, R. D. Johnson, and J. R. Salem	Process for Making Fullerenes
5,275,734	J. A. Shurtleff and K. P. Unger	Chemical Pre-treatment and Biological Destruction of Propylene Carbonate Waste Streams Effluent Streams to Reduce the Biological Oxygen Demand Thereof
5,275,963	C. Cederbaum, R. Chanclo, M. Combes, and P. Mone	Method of Forming Stacked Conductive and/or Resistive Polysilicon Lands in Multilevel Semiconductor Chips and Structures Resulting Therefrom
5,276,314	M. J. Martino, G. L. Meissner, and R. C. Paulsen, Jr.	Identity Verification System Resistant to Compromise by Observation of Its Use
5,276,337	A. Starikov	Accuracy of Alignment and O/L Measurement Systems by Means of Tunable Source and Handling of Signal
5,276,338	K. Beyer, C.-M. Hsieh, L. L. Hsu, and T.-D. Yuan	Bonded Wafer Structure Having a Buried Insulation Layer

5,276,363	W. S. Klara and F. A. Montegari	Zero Power Decoder/Driver
5,276,437	T. A. Horvath and I. Chen	Multi-media Window Manager
5,276,458	D. Sawdon	Display System
5,276,577	P. E. Brooks and J. M. Rigotti	Head-Disk Enclosure Seal for Magnetic Disk Storage Device
5,276,590	M. Budman, M. J. Kuzawinski, and D. M. Saunders	Flex Circuit Electronic Cards
5,276,638	R. C. Wong	Bipolar Memory Cell with Isolated PNP Load
5,276,641	E. J. Sprogis and H. Wong	Hybrid Open Folded Sense Amplifier Architecture for a Memory Device
5,276,682	H. Van As and W. Lemppenau	Medium Access Technique for LAN Systems
5,276,684	R. B. Pearson	High Performance I/O Processor
5,276,766	L. R. Bahl, J. R. Bellegarda, P. V. DeSouza, D. Nahamoo, and M. A. Picheny	Fast Algorithm for Deriving Acoustic Prototypes for Automatic Speech Recognition
5,276,776	C. R. Grady, F. D. Highland, C. T. Iwaskiw, and M. Pfeifer	System and Method for Building a Computer-Based Rete Pattern Matching Network
5,276,783	G. C. Fossum	Tessellating Complex Polygons in Modeling Coordinates
5,276,791	D. M. Palmer	Network Editing System
5,276,793	K. W. Borgendale and D. J. Dobkin	System and Method for Editing a Structured Document to Preserve the Intended Appearance of Document Elements
5,276,801	J. G. Heyen, C. Kasiraj, L. A. Melkus, and T. J. Wolf	Data Processing System for Notifying a Terminal User of an Asynchronous Event Occurrence
5,276,808	D. P. Cheney, R. J. Yagley, Jr., M. J. Wolski, A. E. Petruski, and J. A. Boston	Data Storage Buffer System and Method
5,276,813	J. C. Elliott, E. P. Hefferon, A. S. Meritt, M. W. Sachs, and M. C. Snedaker	Acquiring Addresses in an Input/Output System
5,276,814	D. G. Bourke, D. R. Chisholm, G. D. Float, R. A. Kelley, R. Y. Liu, C. A. Malmquist, J. M. Nelson, C. B. Perkins, Jr., R. L. Place, H. R. Schwermer, and J. D. Wilson	Method for Transferring Information Between Main Store and Input Output Bus Units Via a Sequence of Asynchronous Bus and Two Synchronous Buses
5,276,816	C. J. Cavendish and S. C. Ramos	Icon Object Interface System and Method
5,276,835	C. Mohan and I. S. Narang	Non-blocking Serialization for Caching Data in a Shared Cache
5,276,838	C. L. Rao and S. W. White	Dynamically Repositioned Memory Bank Queues
5,276,846	F. J. Aichelmann, Jr., B. E. Bachman, R. E. Busch, T. M. Redman, and E. P. Thoma	Fast Access Memory Structure
5,276,848	P. W. Gallagher, S. L. Gregor, and S. M. Reeve	Shared Two Level Cache Including Apparatus for Maintaining Storage Consistency
5,276,864	L. A. Hernandez, M. E. Medford, and E. Tashakori	Personal Computer with Alternate System Controller Error Detection
5,276,869	B. A. Forrest, D. J. Hager, and C. G. Rose	System for Selecting Document Recipients as Determined by Technical Content of Document and for Electronically Corroborating Receipt of Document
5,276,876	A. Coleman, J. A. Henry, B. A. M. Maslak, E. A. Pruul, J. M. Showalter, R. L. Stone, T. J. Szczygielski, and M. E. Vendryes	Registration of Resources for Commit Procedures
5,276,878	P. G. Sutton, W. R. Cohen, and R. A. Blackburn	Method and System for Task Memory Management in a Multi-tasking Data Processing System
5,276,882	P. G. Emma, J. W. Knight, J. H. Pomerene, R. N. Rechtschaffen, F. J. Sparacio, and C. F. Webb	Subroutine Return Through Branch History Table
5,276,883	H. Halliwell	System and Method for Uniform Control of Local and Remote Applications in a Data Processing Network
5,276,898	H. G. Keil and R. J. Manulik	System for Selectively Compressing Data Frames Based Upon a Current Processor Work Load Identifying Whether the Processor Is Too Busy to Perform the Compression
5,276,901	W. E. Howell, H. N. Reddy, and D. S. Wang	System for Controlling Group Access to Objects Using Group Access Control Folder and Group Identification As Individual User
5,276,964	H. R. Anderson, Jr., A. Bross, J. G. Cempa, R. O. Lussow, D. E. Myers, and T. J. Walsh	Method of Manufacturing a High Density Connector System
5,277,749	J. H. Griffith, J. I. Kim, T. L. Leong, W. J. Tilly, and S. Wacks	Methods and Apparatus for Relieving Stress and Resisting Stencil Delamination When Performing Lift-off Processes That Utilize High Stress Metals and/or Multiple Evaporation Steps
5,277,985	J. Li, E. Colgan, and J. W. Mayer	Process for Fabricating Copper Interconnects in Ultra Large Scale Integrated (ULSI) Circuits
5,278,010	R. A. Day, J. D. Gelorme, D. J. Russell, and S. Jo	Composition for Photo Imaging

5,278,136	J. G. Bednorz, J. D. Mannhart, and C. A. Mueller	Method for Making a Superconducting Field-Effect Transistor with Inverted MISFET Structure
5,278,140	P. Chaudhari, C.-C. J. Chi, D. B. Dimos, J. D. Mannhart, and C. C. Tsuei	Method for Forming Grain Boundary Junction Devices Using High T_c Superconductors
5,278,456	R. A. Béchade and B. A. Kauffmann	Process Independent Digital Clock Signal Shaping Network
5,278,711	T. A. Gregory, C. G. Keller, and T. S. Larson	Slider Bearing Design for Near Contact Magnetic Recording
5,278,724	C. G. Angulas and T. E. Kindl	Electronic Package and Method of Making Same
5,278,751	C. A. Adiano, R. G. Raines, Jr., and R. B. Rowen	Dynamic Manufacturing Process Control
5,278,800	W. W. Grunbok, B. J. Knowles, W. R. Milani, D. R. Moran, D. E. Pontius, D. W. Price, R. Tamlyn, Y.-M. Ting, D. Tran, and H. Yeh	Memory System and Unique Memory Chip Allowing Island Interlace
5,278,838	S. W. Ng, D. W. Palmer, and R. S. Thompson	Recovery from Errors in a Redundant Array of Disk Drives
5,278,841	D. C. Myers	Method and Apparatus for Diagnosing Net Interconnect Faults Using Echo Pulsed Signals
5,278,842	R. W. Berry, Jr. and J. Savir	Delay Test Coverage Enhancement for Logic Circuitry Employing Level Sensitive Scan Design
5,278,909	A. D. Edgar	System and Method for Stereo Digital Audio Compression with Co-channel Steering
5,278,927	Y. Birk	Efficient Single-hop Interconnection Network Employing Merged Shared Directional Multichannels
5,278,942	L. R. Bahl, J. R. Bellegarda, P. V. De Souza, P. S. Gopalakrishnan, A. J. Nadas, D. Nahamoo, and M. A. Picheny	Speech Coding Apparatus Having Speaker Dependent Prototypes Generated from Nonuser Reference Data
5,278,948	W. L. Luken, Jr.	Parametric Surface Evaluation Method and Apparatus for a Computer Graphics Display System
5,278,955	R. W. Forté and E. J. Stokes	Open Systems Mail Handling Capability in a Multi-user Environment
5,278,963	J. R. Hattersley and T. D. Kim	Pretranslation of Virtual Addresses Prior to Page Crossing
5,278,967	B. W. Curran	System for Providing Gapless Data Transfer from Page-Mode Dynamic Random Access Memories
5,278,970	J. W. Pence	Method for Efficient Utilization of Removable Data Recording Media
5,278,978	R. A. Demers, B. G. Lindsay, R. A. Reinsch, and M. R. Zimowski	Method and System for Describing and Exchanging Data Between Heterogeneous Database Systems with Data Converted by the Receiving Database System
5,278,979	R. D. Foster and E. K. McCauley	Version Management System Using Pointers Shared by a Plurality of Versions for Indicating Active Lines of a Version
5,278,982	D. S. Daniels, W. A. Sawdon, and R. L. Haskin	Log Archive Filtering Method for Transaction-Consistent Forward Recovery from Catastrophic Media Failures
5,279,309	R. H. Taylor and Y. Kim	Signaling Device and Method for Monitoring Positions in a Surgical Operation
5,279,632	D. W. Decker and E. L. Pepe	Planar Clean Room Ceiling Structure
5,279,669	Y. H. Lee	Plasma Reactor for Processing Substrates Comprising Means for Inducing Electron Cyclotron Resonance (ECR) and Ion Cyclotron Resonance (ICR) Conditions
5,279,711	J. A. Frankeny, R. F. Frankeny, and J. LaTorre	Chip Attach and Sealing Method
5,279,926	W. F. Chandler, R. D. Johnson, S. E. Monahan, and R. W. Olson	Method and Apparatus for Removing Vapor from a Pressurized Sprayed Liquid in the Manufacture of Semiconductor Integrated Circuits
5,279,987	J. S. Lechaton, S.-N. Mei, D. J. Schepis, and M. M. Smadi	Fabricating Planar Complementary Patterned Subcollectors with Silicon Epitaxial Layer
5,280,154	J. J. Cuomo and C. R. Guarnieri	Radio Frequency Induction Plasma Processing System Utilizing a Uniform Field Coil
5,280,192	B. R. Kryzaniwsky	Three-Dimensional Memory Card Structure with Internal Direct Chip Attachment
5,280,204	R. R. Livolsi	ECI Compatible CMOS Off-Chip Driver Using Feedback to Set Output Levels
5,280,275	C. A. Kaplan	Graphical Interface Control Buttons with Scalar Values
5,280,341	M. Nonnenmacher, M. VaeziIravani, and H. K. Wickramasinghe	Feedback Controlled Differential Fiber Interferometer
5,280,344	M. N. Witlin, D. A. Bresson, M. J. Buehler, R. J. Buratti, O. E. Kline III, K. A. Rhrer, and J. Rio	Method and Means for Adding an Extra Dimension to Sensor Processed Raster Data Using Color Encoding
5,280,406	H. J. Coufal and R. K. Grygier	Jet Deposition of Electrical Charge on a Dielectric Surface
5,280,414	C. R. Davis, R. Hsiao, J. R. Loomis, J. M. Park, and J. D. Reid	Au-Sn Transient Liquid Bonding in High Performance Laminates